

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-II (EC-A)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1 (AC)	B2 (PC)	C3 (DSA)	D4 (T) (Maths-III)	L U N C H	G5	H6	I7	J8
	P1		P2			P11 (DSA-A2/DIC-A1)		P12	
Tuesday	B1 (PC)	C2 (DSA)	D3 (DIC)	E4 (S&S)		H5	I6	J7	K8
	P3		P4			P13 (DSA-A3/DIC-A2)		P14 (AC-A1/PC-A3)	
Wednesday	C1 (Maths-III)	D2 (DIC)	E3 (T) (S&S)	A4 (Maths-III)		I5	J6	K7	G8
	P5		P6			P15 (AC-A2/PC-A1)		P16 (AC-A3/PC-A2)	
Thursday	D1 (DIC)	E2 (S&S)	A3 (AC)	B4 (Maths-III)		J5	K6	G7	H8
	P7		P8			P17		P18 (DSA-A1/DIC-A3)	
Friday	E1 (S&S)	A2 (AC)	B3 (PC)	C4 (DSA)		K5	G6	H7	I8
	P9		P10			P19		P20	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGO RY	CREDIT	CLASS ROOM
1	A1,A2,A3	EC 201	Analog Circuits (AC)	AA,AA,AA	3 -0- 2	4	EC022N
2	E1,E2,E4 (T):E3	EC 203	Signals and Systems(S&S)	KPU,KPU,KPU (T):JNP,L1,P12	3 -1- 0	4	EC022N
3	C2,C3,C4	EC 205	Data Structures and Algorithms(DSA)	RP,RP,RP	3 -0- 2	4	EC022N
4	D1,D2,D3	EC 207	Digital Integrated Circuits(DIC)	SY,SY,SY	3 -0- 2	4	EC022N
5	B1,B2,B3	EC 209	Principles of Communication Systems(PC)	SG,SG,SG	3 -0- 2	4	EC022N
6	A4,B4,C1 (T): D4	MA 217	Mathematics-III (Maths-III)	SKS,SKS,SKS SKS,LS,EJ	3 -1- 0	4	EC022N
7	P14	EC 201	Analog Circuits -A1	L4,P17,P26,P4	-	1	EC302N
8	P15	EC 201	Analog Circuits -A2	AA,L4,P4,P9	-	1	EC302N
9	P16	EC 201	Analog Circuits -A3	NBK,L4,P9,P26,MSV16	-	1	EC302N
10	P18	EC 205	Data Structures and Algorithms -A1	PKS,L1,P25	-	1	EC307N
11	P11	EC 205	Data Structures and Algorithms -A2	L1,P25,MSC7	-	1	EC307N
12	P13	EC 205	Data Structures and Algorithms -A3	PKS,L1,P18	-	1	EC307N
13	P11	EC 207	Digital Integrated Circuits -A1	SY,L3,P21,MSV7	-	1	EC406N
14	P13	EC 207	Digital Integrated Circuits -A2	VG,L3,P21	-	1	EC406N
15	P18	EC 207	Digital Integrated Circuits -A3	SY,L3,P17,MSV21	-	1	EC406N
16	P15	EC 209	Principles of Communication Systems -A1	SG,L2,P6,P19	-	1	EC402N
17	P16	EC 209	Principles of Communication Systems -A2	SD,L2,P6,P19	-	1	EC402N
18	P14	EC 209	Principles of Communication Systems -A3	JNP,L2,P6,P15	-	1	EC402N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-II (EC-B)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B2	C3	D4	L U N C H	G5 (AC)	H6 (PC)	I7 (DIC)	J8 (Maths-III)
	P1		P2			P11		P12	
Tuesday	B1	C2	D3	E4		H5 (PC)	I6 (DIC)	J7 (DSA)	K8 (S&S)
	P3		P4			P13		P14	
Wednesday	C1	D2	E3	A4		I5 (DIC)	J6 (Maths-III)	K7(T) (S&S)	G8 (DSA)
	P5		P6			P15		P16	
Thursday	D1	E2	A3	B4		J5 (Maths-III)	K6 (S&S)	G7 (AC)	H8 (DSA)
	P7 (DIC-A4/DSA-A5)		P8 (PC-A5/AC-A4)			P17		P18	
Friday	E1	A2	B3	C4		K5 (S&S)	G6 (AC)	H7 (PC)	I8 (T) (Maths-III)
	P9 (DIC-A5/DSA-A4)		P10 (PC-A4/AC-A5)			P19		P20	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASS ROOM
1	G5,G6,G7	EC 201	Analog Circuits (AC)	AA,AA,AA	3-0-2	4	EC022N
2	K5,K6,K8 (T):K7	EC 203	Signals and Systems(S&S)	KPU,KPU,KPU (T): JNP,L1,P12	3-1-0	4	EC022N
3	G8,H8,J7	EC 205	Data Structures and Algorithms(DSA)	RP,RP,RP	3-0-2	4	EC022N
4	I5,I6,I7	EC 207	Digital Integrated Circuits(DIC)	SY,SY,SY	3-0-2	4	EC022N
5	H5,H6,H7	EC 209	Principles of Communication Systems(PC)	SG,SG,SG	3-0-2	4	EC022N
6	J5,J6,J8 (T): I8	MA 217	Mathematics-III (Maths-III)	TA3,TA3,TA3 TA3,LS,KD,SM	3-1-0	4	EC022N
7	P8	EC 201	Analog Circuits -A4	NBK,L4,P4,P17	-	1	EC302N
8	P10	EC 201	Analog Circuits -A5	PJE,L4,P20,MSV16	-	1	EC302N
9	P9	EC 205	Data Structures and Algorithms -A4	RP,L1,P18	-	1	EC307N
10	P7	EC 205	Data Structures and Algorithms -A5	L1,P25,MSC7	-	1	EC307N
11	P7	EC 207	Digital Integrated Circuits -A4	L3,P2, MSV7	-	1	EC406N
12	P9	EC 207	Digital Integrated Circuits -A5	VG,L3,P21,MSV21	-	1	EC406N
13	P10	EC 209	Principles of Communication Systems -A4	JNP,L2,P15,P19	-	1	EC402N
14	P8	EC 209	Principles of Communication Systems -A5	SG,L2,P6,P19	-	1	EC402N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-III (EC-A)

Day	1	2	3	4		5	6	7	8	9	10
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50	6.00-6.50	7.00-7.50
Monday	A1 (DSP)	B2 (IE-I) (S&T)	C3 (D.COM)	D4 (TL&EW)	L U N C H	G5	H6 (CE-I)	I7	J8		
	P1		P2			P11		P12 (TL&EW-A2 \ DSP-A3 \ D.COM-A1)		P21	
Tuesday	B1	C2 (D.COM)	D3 (T) (TL&EW)	E4 (T) (PEEBM)		H5 (Seminar)	I6	J7	K8		
	P3		P4			P13		P14		P22 (D.COM-A3)	
Wednesday	C1 (T) (D.COM)	D2 (TL&EW)	E3 (PEEBM)	A4(T) (DSP)		I5	J6	K7	G8		
	P5		P6			P15 (DSP-A1)		P16 (DSP-A2)		P23	
Thursday	D1 (TL&EW)	E2 (PEEBM)	A3 (DSP)	B4 (IE-I) (S&T)		J5 (CE-I)	K6	G7	H8		
	P7		P8			P17		P18 (TL&EW-A1)		P24 (D.COM-A2)	
Friday	E1 (PEEBM)	A2 (DSP)	B3 (IE-I) (S&T)	C4 (D.COM)		K5	G6	H7 (CE-I)	I8 (Seminar)		
	P9		P10			P19		P20		P25 (TL&EW-A3)	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1	D1,D2,D4 (T):D3	EC 301	Transmission Lines and Electromagnetic Waves(TL&EW)	DS,DS,DS (T):L2,P22,P23	3-1-2	4	EC106N
2	C2,C3,C4 (T): C1	EC 303	Digital Communication(D.COM)	UDD,UDD,UDD (T):L9,P7,P25	3-1-2	4	EC106N
3	A1,A3,A2 (T):A4	EC 305	Digital Signal Processing(DSP)	MCP,MCP,MCP (T):L3,P13,P27	3-1-2	4	EC106N
4	E1,E2,E3 (T): E4	HU 301	Professional Ethics, Economics and Business Management(PEEBM)	KT,KT,KT UK,HB,SS	3-1-0	4	EC106N
5	J5,H6,H7 Core Elective-I (CE-I)	EC 321	Computer Architecture and Organization(CAO)	PKS,PKS,PKS	3-0-0	3	EC106N
6		EC 323	Data Structures and Algorithms(DSA)	RP,RP,RP	3-0-0	3	EC021N
7		EC 325	VLSI Technology(VT)	VG,VG,VG	3-0-0	3	EC222N
8		EC 327	Digital Image Processing(DIP)	JNS,JNS,JNS	3-0-0	3	EC105N
9	B2,B3,B4 Institute Elective-I (IE-I)	EC 361	Sensors and Transducers(S&T)	PNP,PNP,PNP	3-0-0	3	EC106N
10	H5,I8	EC 307	Seminar	-	0-0-2	1	
11	P18	EC 301	Transmission Lines and Electromagnetic Waves -A1	ASM,P7,P22,P23	-	1	EC403N
12	P12	EC 301	Transmission Lines and Electromagnetic Waves -A2	ASM,L2,P10,P23	-	1	EC403N
13	P25	EC 301	Transmission Lines and Electromagnetic Waves -A3	DS,L2,P22,P23	-	1	EC403N
14	P12	EC 303	Digital Communication -A1	SNS,L6,L9	-	1	EC402N
15	P24	EC 303	Digital Communication -A2	L9,P18	--	1	EC402N
16	P22	EC 303	Digital Communication -A3	L9,P18	-	1	EC402N
17	P15	EC 305	Digital Signal Processing -A1	MCP,L7,P12	-	1	EC307N
18	P16	EC 305	Digital Signal Processing -A2	L3,P13,P27	-	1	EC307N
19	P12	EC 305	Digital Signal Processing -A3	KPU,L1,L7,P12	-	1	EC307N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-III (EC-B)

Day	1	2	3	4		5	6	7	8	9	10
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50	6.00-6.50	7.00-7.50
Monday	A1	B2 (IE-I) (S&T)	C3	D4	L U N C H	G5 (PEEBM)	H6 (CE-I)	I7 (DSP)	J8 (TL&EW)	K9 (T) (D.COM)	
	P1		P2 (TL&EW-A4/DSP-A5)			P11		P12		P21	
Tuesday	B1	C2 (Seminar)	D3	E4		H5	I6 (DSP)	J7 (PEEBM)	K8 (TL&EW)	G9	
	P3		P4 (TL&EW-A5/DSP-A4)			P13		P14		P22	
Wednesday	C1	D2	E3	A4		I5 (T) (DSP)	J6 (T) (PEEBM)	K7	G8 (D.COM)	H9 (TL&EW)	
	P5(D.COM-A5)		P6(D.COM-A4)			P15		P16		P23	
Thursday	D1	E2 (Seminar)	A3	B4(IE-I) (S&T)		J5 (CE-I)	K6	G7	H8 (D.COM)	I9 (T) (TL&EW)	
	P7		P8			P17		P18		P24	
Friday	E1	A2	B3(IE-I) (S&T)	C4		K5 (PEEBM)	G6 (DSP)	H7 (CE-I)	I8 (D.COM)	J9	
	P9		P10			P19		P20		P25	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1	J8,K8,H9 (T): I9	EC 301	Transmission Lines and Electromagnetic Waves(TL&EW)	DS,DS,DS (T):L2,P22,P23	3-1-2	4	EC106N
2	G8,H8,I8 (T): K9	EC 303	Digital Communication(D.COM)	UDD,UDD,UDD ((T):L9,P7,P25	3-1-2	4	EC106N
3	I7,I6,G6 (T): I5	EC 305	Digital Signal Processing(DSP)	MCP,MCP,MCP (T):L3,P13,P27	3-1-2	4	EC106N
4	G5,J7,K5 (T): J6	HU 301	Professional Ethics, Economics and Business Management(PEEBM)	VD,RL,RL	3-1-0	4	EC106N
5	Core Elective-I (CE-I)	EC 321	Computer Architecture and Organization(CAO)	PKS,PKS,PKS	3-0-0	3	EC106N
6		EC 323	Data Structures and Algorithms(DSA)	RP,RP,RP	3-0-0	3	EC021N
7		EC 325	VLSI Technology(VT)	VG,VG,VG	3-0-0	3	EC222N
8		EC 327	Digital Image Processing(DIP)	JNS,JNS,JNS	3-0-0	3	EC105N
9	B2,B3,B4	EC 361	Sensors and Transducers(S&T)	PNP,PNP,PNP	3-0-0	3	EC106N
10	C2,E2	EC 307	Seminar	-	0-0-2	1	
11	P2	EC 301	Transmission Lines and Electromagnetic Waves -A4	L5,P10,P22	-	1	EC403N
12	P4	EC 301	Transmission Lines and Electromagnetic Waves -A5	DS,L5,P10	-	1	EC403N
13	P6	EC 303	Digital Communication -A4	L9,P7,MSC8	-	1	EC402N
14	P5	EC 303	Digital Communication -A5	SNS,L9,P7	-	1	EC402N
15	P4	EC 305	Digital Signal Processing -A4	KPU,L3,P13,P27	-	1	EC307N
16	P2	EC 305	Digital Signal Processing -A5	MCP,L1,P12,P27	-	1	EC307N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-IV (EC-A)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1 (ME)	B2 (VD)	C3 (CE-V)	D4 (CE-IV)	L U N C H	G5 (Project)	H6	I7 (Project)	J8
	P1		P2			P11		P12	
Tuesday	B1 (VD)	C2 (DL)	D3 (CE-VI)	E4 (CE-IV)		H5	I6 (Project)	J7	K8 (Project)
	P3		P4			P13		P14	
Wednesday	C1 (CE-VI)	D2 (DL)	E3(T) (VD)	A4 (ME)		I5 (Project)	J6 (Project)	K7	G8
	P5		P6			P15		P16	
Thursday	D1 (CE-IV)	E2 (CE-VI)	A3 (ME)	B4 (CE-V)		J5	K6	G7	H8
	P7		P8			P17 (ME-A1/DL-A2/VD-A3)		P18	
Friday	E1 (CE-V)	A2 (T) (ME)	B3 (VD)	C4 (DL)		K5	G6	H7	I8
	P9		P10			P19 (ME-A2/DL-A3/VD-A1)		P20 (ME-A3/DL-A1/VD-A2)	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASS ROOM
1	A1,A3,A4 (T):A2	EC 401	Microwave Engineering(ME)	GS,GS,GS (T):L5,P8,P24	3-1-2	4	EC203N
2	B2,B3,B1 (T):E3	EC 403	VLSI Design(VD)	DJ,DJ,DJ (T):L6,P16,P20	3-1-2	4	EC203N
3	D2,C2,C4	EC 405	Deep Learning(DL)	KC,KC,KC	3-0-2	4	EC203N
4	D4,E4,D1 Core Elective-IV (CE-IV)	EC 421	Optical Wireless Communication(OWC)	ASM,ASM,ASM	3-0-0	3	EC105N
5		EC 427	Fundamentals of Nanoelectronics(Nano)	NC,NC,NC	3-0-0	3	EC021N
6		EC 429	Processor Architecture(PA)	PJE,PJE,PJE	3-0-0	3	EC203N
7	C3,B4,E1 Core Elective-V (CE-V)	EC 431	Error Control Coding(ECC)	JNP,JNP,JNP	3-0-0	3	EC203N
8		EC 433	EM Interference and Compatibility(EMI&C)	KI,KI,KI	3-0-0	3	EC021N
9		EC 435	Global Navigation Satellite System(GNSS)	SNS,SNS,SNS	3-0-0	3	EC105N
10	D3,C1,E2 Core Elective-VI (CE-VI)	EC 441	MIMO Communication systems(MIMO)	SNS,SNS,SNS	3-0-0	3	EC105N
11		EC 445	Estimation and Detection Theory(EDT)	PKS,PKS,PKS	3-0-0	3	EC203N
12		EC 447	Speech Processing and Human-Machine Communication(SPMC)	SD,SD,SD	3-0-0	3	EC021N
13	G5,I7,I6,K8,I5,J6	EC 407	Project	-	0-0-6	3	
14	P17	EC 401	Microwave Engineering -A1	L5,P8,MSC10	-	1	EC403N
15	P19	EC 401	Microwave Engineering -A2	PNP,L5,L9,P8	-	1	EC403N
16	P20	EC 401	Microwave Engineering -A3	GS,L5,P24	-	1	EC403N
17	P19	EC 403	VLSI Design -A1	DJ,L6,P20	-	1	EC406N
18	P20	EC 403	VLSI Design -A2	DJ,L6,P16,MSV19	-	1	EC406N
19	P17	EC 403	VLSI Design -A3	NC,L6,P16	-	1	EC406N
20	P20	EC 405	Deep Learning -A1	L7,P5	-	1	EC307N
21	P17	EC 405	Deep Learning -A2	KC,L7,P5	-	1	EC307N
22	P19	EC 405	Deep Learning -A3	L7,P5,P15	-	1	EC307N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: B. TECH-IV (EC-B)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1	B2	C3 (CE-V)	D4 (CE-IV)	L U N C H	G5 (T) (ME)	H6 (VD)	I7	J8
	P1(VD-A4/ME-A5)		P2			P11		P12	
Tuesday	B1	C2	D3 (CE-VI)	E4 (CE-IV)		H5 (VD)	I6 (DL)	J7 (Project)	K8
	P3(VD-A5/ME-A4)		P4			P13		P14	
Wednesday	C1 (CE-VI)	D2 (Project)	E3	A4		I5 (T) (VD)	J6 (ME)	K7 (DL)	G8
	P5		P6(DL-A4)			P15		P16	
Thursday	D1 (CE-IV)	E2 (CE-VI)	A3 (Project)	B4 (CE-V)		J5 (ME)	K6 (VD)	G7 (Project)	H8
	P7		P8			P17		P18	
Friday	E1 (CE-V)	A2 (Project)	B3	C4		K5 (ME)	G6 (DL)	H7 (Project)	I8
	P9		P10(DL-A5)			P19		P20	

Sl. No.	SLOT	COURSE NO	TITLE	FACULTY	CATEGOR Y	CREDIT	CLASS ROOM
1	J6,K5,J5 (T):G5	EC 401	Microwave Engineering(ME)	GS,GS,GS (T):L5,P8,P24	3-1-2	4	EC203N
2	H5,H6,K6 (T):I5	EC 403	VLSI Design(VD)	DJ,DJ,DJ (T):L6P16,P20	3-1-2	4	EC203N
3	G6,I6,K7	EC 405	Deep Learning(DL)	KC,KC,KC	3-0-2	4	EC203N
4	D4,E4,D1 Core Elective-IV (CE-IV)	EC 421	Optical Wireless Communication(OWC)	ASM,ASM,ASM	3-0-0	3	EC105N
5		EC 427	Fundamentals of Nanoelectronics(Nano)	NC,NC,NC	3-0-0	3	EC021N
6		EC 429	Processor Architecture(PA)	PJE,PJE,PJE	3-0-0	3	EC203N
7	C3,B4,E1 Core Elective-V (CE-V)	EC 431	Error Control Coding(ECC)	JNP,JNP,JNP	3-0-0	3	EC203N
8		EC 433	EM Interference and Compatibility(EMI&C)	KI,KI,KI	3-0-0	3	EC021N
9		EC 435	Global Navigation Satellite System(GNSS)	SNS,SNS,SNS	3-0-0	3	EC105N
10	D3,C1,E2 Core Elective-VI (CE-VI)	EC 441	MIMO Communication systems(MIMO)	SNS,SNS,SNS	3-0-0	3	EC105N
11		EC 445	Estimation and Detection Theory(EDT)	PKS,PKS,PKS	3-0-0	3	EC203N
12		EC 447	Speech Processing and Human-Machine Communication(SPMC)	SD,SD,SD	3-0-0	3	EC021N
13	D2,A2,A3,G7, H7,J7	EC 407	Project	-	0-0-6	3	
14	P3	EC 401	Microwave Engineering(ME) EC-A4	GS,L5,P8	-	1	EC403N
15	P1	EC 401	Microwave Engineering(ME) EC-A5	L5,L9,MSC10	-	1	EC403N
16	P1	EC 403	VLSI Design(VD) EC-A4	NC,L6,P16,MSV10	-	1	EC406N
17	P3	EC 403	VLSI Design(VD) EC-A5	L6,P20,MSV10,MSV19	-	1	EC406N
18	P6	EC 405	Deep Learning(DL) EC-A4	KC,L7,P15	-	1	EC307N
19	P10	EC 405	Deep Learning(DL) EC-A5	L7,P5,MSC8	-	1	EC307N

TIME TABLE

Semester: ODD
Year: 2023-2024

Class: M. TECH-I (CS &VL)

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30-9.20	9.30-10.20	10.30-11.20	11.30-12.20	12.20-2.00	2.00-2.50	3.00-3.50	4.00-4.50	5.00-5.50
Monday	A1 (ADSP)	B2 (ES)	C3 (ITC)	D4 (VT)	L U N C H	G5(DSC) (HDL)	H6 (DMS)	I7	J8
	P1		P2			P11		P12(HDL)	
Tuesday	B1	C2	D3 (RFCS)	E4(AOCS) (MOS)		H5 (HDL)	I6 (DMS)	J7	K8
	P3 (ADSP/DVD)		P4			P13		P14(RFCS/ES)	
Wednesday	C1 (RFCS)	D2 (VT)	E3(AOCS) (MOS)	A4 (ITC)		I5	J6	K7	G8
	P5		P6			P15(RFCS)		P16(AOCS)	
Thursday	D1 (VT)	E2(AOCS) (MOS)	A3(DSC) (HDL)	B4 (ADSP)		J5 (ES)	K6 (RFCS)	G7 (DVD)	H8
	P7		P8			P17		P18	
Friday	E1 (DVD)	A2 (DVD)	B3 (ADSP)	C4 (ITC)		K5 (ES)	G6(DSC) (DMS)	H7	I8
	P9		P10			P19		P20(MOS)	

Sl. No.	SLOT	COURS E NO	TITLE	FACULTY	CATEGORY	CREDIT	CLASSROOM
1	C1,D3,K6	EC 601	RF Circuits & Systems(RFCS)	KI,KI,KI	3-0-0	3	EC201N
2	E2,E3,E4	EC 603	Advance Optical Communication Systems(AOCS)	PNP,PNP,PNP	3-0-0	3	EC201N
3	A1,B3,B4	EC 605	Advance DSP(ADSP)	SD,SD,SD	3-0-0	3	EC201N
4	G7,A2,E1	EC609	Digital VLSI Design(DVD)	ADD,ADD,ADD	3-0-0	3	EC201N
5	B2,J5,K5	EC613	Embedded Systems(ES)	PJE,PJE,PJE	3-0-0	3	EC201N
6	D1,D2,D4	EC617	VLSI Technology(VT)	VG,VG,VG	3-0-0	3	EC201N
7	C3,A4,C4	EC619	Information Theory & Coding(ITC)	JNS,JNS,JNS	3-0-0	3	EC201N
8	A3,G5	EC621	Digital Satellite Communication(DSC)	ASM,ASM,ASM	3-0-0	3	EC201N
	G6						EC222N
9	E2,E3,E4	EC 611	MOS Devices And Technology(MOS)	RND,RND,RND	3-0-0	3	EC222N
10	A3,H5,G5	EC623	Hardware Description Language(HDL)	ADD,ADD,ADD	3-0-0	3	EC222N
11	G6,H6,I6	EC625	Device Modeling & Simulation(DMS)	NC,NC,NC	3-0-0	3	EC201N
12	P15,P14	EC 601	RF Circuits & Systems	KI,PH24	-	1	EC403N
13	P16	EC 603	Advance Optical Communication Systems	PNP,PH10	-	1	EC403N
14	P3	EC 605	Advance DSP	SD,PH13	-	1	EC307N
15	P3	EC609	Digital VLSI Design	ADD,PH17	-	1	EC306N
16	P14	EC613	Embedded Systems	PJE,PH3	-	1	EC307N
17	P20	EC 611	MOS Devices And Technology	AA,PH9	-	1	EC306N
18	P12	EC623	Hardware Description Language	ADD,PH26	-	1	EC306N